

Novel Applications of VDVTAs: As Current-Mode SIMO-Type Biquad and Electronically Controllable Sinusoidal Oscillator

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Abstract

In this paper we present new applications of voltage differencing voltage transconductance amplifier (VDVTA) as current-mode (CM) biquad filter and electronically controllable sinusoidal oscillator (ECSO) using single VDVTA and with four passive components, three passive components respectively. The biquad circuit realizes low pass, high pass, and band pass filter simultaneously without changing the circuit topology. The oscillator circuit also enjoys the explicit current output and also the frequency is electronically controllable. The active and passive sensitivities for both circuits are very low. Both the circuits are simulated using PSPICE with TSMC CMOS 0.18 μm process parameter

Keywords: VDVTA, CM biquad filter, Electronically Controllable Oscillator

1 Introduction

Due to longer dynamic range and wider bandwidth the current mode (CM) application is more popular as compare to voltage mode (VM) counter parts [1]. Now days the main focus of researcher is to use single active element/ building block

based single input multi output filter and electronically controllable oscillator see [3-21] and the references cited therein. There are many active building block has been represented by BIOLEK, SENANI, BIOLKOVA, KOLKA, in [2]. The multi function filter is more flexible because the same circuit structure can be used for different filter responses. Single Input Multiple Output (SIMO) type filter using different active building block are available in the literature see [3-10]. Sinusoidal oscillator find a number of application in communication, signal processing, control system, instrumentation and measurement system [11-13]. ECSOs based on different active building blocks are available in the literature see [14-21] and the reference cited therein. The advantages and applications of the active building block used in this paper are available in the literature [22-23]. However to the best knowledge and belief of the authors SIMO CM biquad as well as ECSOs using single VDVTA have not been yet presented in the open literature so far.

The purpose of this paper is to propose a new SIMO-type CM biquad and ECSOs using single VDVTA along with bare minimum passive components. The active and passive sensitivities for both the circuits are very low. Both the circuits are simulated using PSPICE with TSMC CMOS 0.18 μm process parameter.

2 The Proposed Configuration

The VDVTA is a recently proposed in [23]. Fig. 1 shows the symbolic representation VDVTA, where P, N and V are input terminals and Z, V, X+ and X- are output terminals. All the terminals of VDVTA are high impedance terminals.

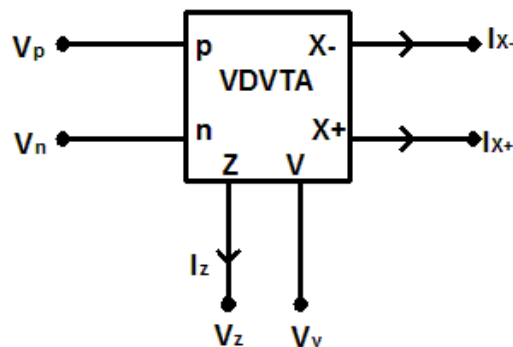


Fig. 1 Symbolic representation of VDVTA [23]

The terminal current voltage relationships of VDVTA can be given by following set of equations:

$$I_z = g_{m_1} (V_p - V_n) \quad (1)$$

$$I_{x+} = g_{m_2} (V_z - V_v) \quad (2)$$

$$I_{x^-} = -g_{m_2}(V_z - V_v) \tag{3}$$

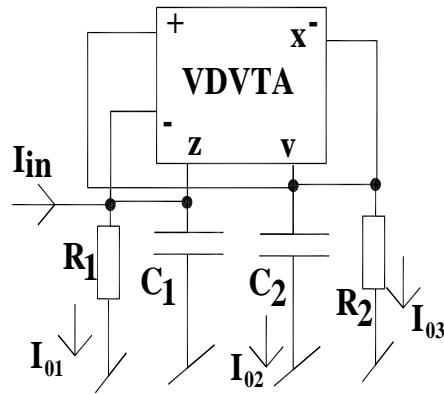


Fig. 2 Proposed SIMO-type CM- Biquad

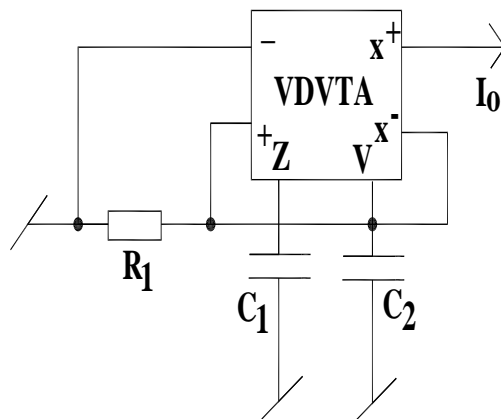


Fig. 3 Proposed CM- Electronically Controlled Sinusoidal Oscillator

The routine circuit analysis of Fig. 2 yields the following filter transfer functions of the universal filters:

$$T_1(s)|_{HP} = \frac{I_{o1}}{I_{in}} = \frac{s^2}{D(s)} \tag{4}$$

$$T_2(s)|_{BP} = \frac{I_{o2}}{I_{in}} = -\frac{s\left(\frac{g_{m_2}}{C_1}\right)}{D(s)} \tag{5}$$

$$T_3(s)|_{LP} = \frac{I_{o3}}{I_{in}} = -\frac{\left(\frac{g_{m_2}}{R_2 C_1 C_2}\right)}{D(s)} \quad (6)$$

Where

$$D(s) = s^2 + \frac{s}{C_1} \left(\frac{1}{R_1} + g_{m_1} \right) + \frac{g_{m_1} g_{m_2}}{C_1 C_2} \quad (7)$$

The natural frequency ω_0 , BW and quality factor Q_0 are given by:

$$\omega_0 = \sqrt{\frac{g_{m_1} g_{m_2}}{C_1 C_2}} \quad (8)$$

$$BW = \frac{1}{C_1} \left(\frac{1}{R_1} + g_{m_1} \right) \quad (9)$$

$$Q_0 = \frac{\sqrt{\frac{g_{m_1} g_{m_2} C_1}{C_2 \left(\frac{1}{R_1} + g_{m_1} \right)^2}}}{\sqrt{\frac{g_{m_1} g_{m_2}}{C_1 C_2}}} \quad (10)$$

A routine circuit analysis of Fig. 3 shows the following characteristic equation:

$$s^2 + s \frac{1}{C_2} \left(\frac{1}{R_2} - g_{m_2} \right) + \frac{g_{m_1} g_{m_2}}{C_1 C_2} = 0 \quad (11)$$

Thus the condition of oscillation (CO) and frequency of oscillation (FO) are given by

$$\left(\frac{1}{R_2} - g_{m_2} \right) \leq 0, \omega_0 = \sqrt{\frac{g_{m_1} g_{m_2}}{C_1 C_2}} \quad (12)$$

Therefore, it is seen that CO is controlled by resistance R_2 and FO is electronically controllable by transconductance g_{m_1} .

Using sensitivity formula $S_x^F = \frac{x}{F} \frac{\partial F}{\partial x}$, the active and passive sensitivities of ω_0

with respect to each elements are found to be $-\frac{1}{2}$.

3 SPICE Simulation Results

To confirm the theoretical analysis, the proposed circuits have been simulated using CMOS VDVTA circuit of Fig. 3 [23]. The passive component values used for SIMO-type CM biquad as shown in Fig. 2 were $C_1 = 0.005\text{nF}$, $C_2 = 0.01\text{nF}$ and $R_1 = 1.583\text{k}\Omega$ and for sinusoidal oscillator as shown in Fig. 3 were

$C_1 = 0.01\text{nF}$, $C_2 = 0.01\text{nF}$ and $R_1 = 1.678\text{k}\Omega$. In all the simulations the bias voltage of VDVTAs are ± 0.9 volts D.C with bias currents $I_{B1} = I_{B2} = I_{B3} = I_{B4} = 150\mu\text{A}$. The simulated frequency response of filter given in Fig. 2 is shown in Fig. 4. The transient response, steady response and the output spectrum of the sinusoidal oscillator of Fig. 3 is shown in Fig. 5. The comparison of circuit shown in Fig. 3 with other available ECSOs is shown in Table 1. These results, thus, confirm the validity of the proposed biquad and oscillator circuits.

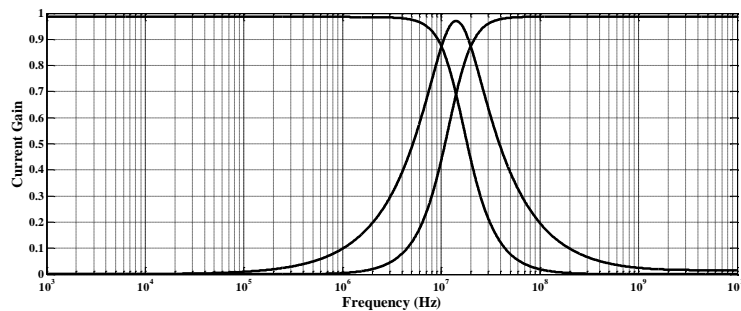
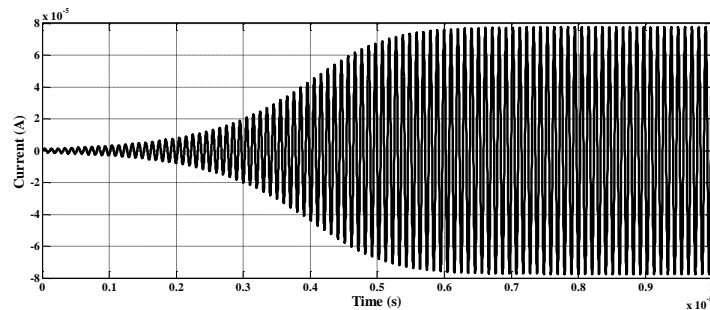
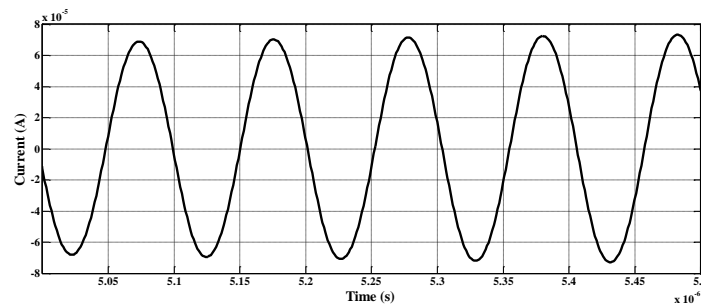


Fig. 4 Frequency response of proposed biquad of Fig. 2



(a)



(b)

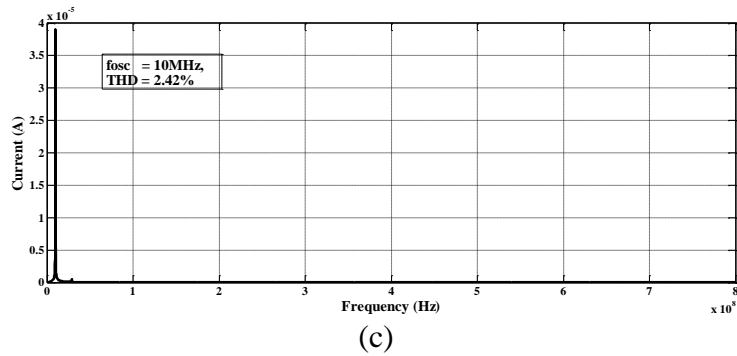


Fig. 5 (a) Transient output waveform, (b) Steady state response of the output and (c) Simulation result of the output spectrum of Fig. 3

Table 1

Reference	Active Component	Grounded Capacitors	Floating Capacitors	Resistors	Availability of Explicit current-mode output	Electronic Tunability
[16]	1	3	0	5	YES	NO
	1	2	1	4	YES	NO
	1	1	2	4	YES	NO
	1	1	1	5	YES	NO
[17]	1	2	0	3	YES	NO
[18]	1	2	0	3	YES	NO
[19]	1	0	3	3	YES	NO
	1	1	2	3	YES	NO
[20]	1	2	0	3	YES	NO
[21]	1	0	2	5	YES	NO
Proposed	1	2	0	1	YES	YES

4 Conclusion

Two new applications of voltage differencing voltage transconductance amplifier are proposed in this paper one is current mode SIMO-type universal biquad and second is electronically controllable sinusoidal oscillator using single active building block. The proposed filter realizes second order low pass, band pass, and high pass filters simultaneously. The other proposed oscillator circuits offers (1) independent control of FO and CO, (2) use of grounded capacitors which is desirable from the viewpoint of IC implementation, (3) availability of explicit CM output and (4) low active and passive sensitivities. Both the circuit offers low active and passive sensitivities of ω_0 . SPICE simulations have established the workability of both the circuits.

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References

- [1] C. Toumazau, F.J. Lidgley, and D.G. Haigh, *Analogue IC Design: The Current-Mode Approach*, London: Peter Peregrinus Limited, 1990.
- [2] D. Biolk, R. Senani, V. Biolkova, Z. Kolka, Active Elements for Analog Signal Processing; Classification, Review and New Proposals, *Radioengineering*, **17** (2008), no. 4, 15-32.
- [3] A. Fabre and J.L. Houle, Voltage-mode and current-mode Sallen-Key implementations based on translinear conveyors, *IEE Proceedings G Circuits, Devices and Systems*, **139** (1992), no. 4, 491-497. <http://dx.doi.org/10.1049/ip-g-2.1992.0077>
- [4] C.M. Chang, C.C Chien, H.Y. Wang, Universal active current filters using single second-generation current conveyor, *Electronics Letters, IEE (UK)*, **29** (1993), no. 13, 1159-1160. <http://dx.doi.org/10.1049/el:19930775>
- [5] E. Yuce , B. Metin and O. Cicekoglu, Current-mode Biquadratic Filters using Single CCIII and Minimum Number of Passive Elements, *Frequenz: J. of RF- Engineering and Telecommunications (Germany)*, **58** (2004), no. 9-10, 225-227. <http://dx.doi.org/10.1515/freq.2004.58.9-10.225>
- [6] B. Chaturvedi, and S. Maheshwari, Current Mode Biquad Filter with Minimum Component Count, *Journal of Active and Passive Electronic Components*, **2011** (2011). <http://dx.doi.org/10.1155/2011/391642>
- [7] D. Biolk, V. Biolkova, and Z. Kolka, Current-mode biquad employing single CDTA, *Indian J. of Pure and Applied Physics*, **47** (2009), 535-537.
- [8] W. Tangsritrat, Novel current-mode and voltage-mode universal biquad filters using single CFTA, *Indian J. of Engineering and Material Sciences*, **17** (2010), 99-104.
- [9] C.N. Lee and C.M. Chang, Single FDCCII-based mixed-mode biquad filter with eight outputs, *AEU: Int. J. of Electronics & Communications (AEU)*, **63** (2009), no. 9, 736-742. <http://dx.doi.org/10.1016/j.aeue.2008.06.015>

- [10] D. Prasad, D.R. Bhaskar, and A.K. Singh, Universal current-mode biquad filter using dual output current differencing transconductance amplifier, *AEU: Int. J. of Electronics & Communications*, **63** (2009), no. 6, 497-501. <http://dx.doi.org/10.1016/j.aeue.2008.02.012>
- [11] R. Senani, New types of sine wave oscillators, *IEEE Transactions on Instrumentation and Measurement* (USA), **34** (1985), no. 3, 461-463. <http://dx.doi.org/10.1109/tim.1985.4315370>
- [12] R. Senani, and D.R. Bhaskar, Single-op-amp sinusoidal oscillators suitable for generation of very low frequencies, *IEEE Trans. on Instrumentation and Measurement* (USA), **40** (1991), no. 4, 777-779. <http://dx.doi.org/10.1109/19.85353>
- [13] D.R. Bhaskar and R. Senani, New CFOA-based single-element- controlled sinusoidal oscillators, *IEEE Trans. on Instrumentation and Measurement* (USA), **55** (2006), no. 6, 2014-2021. <http://dx.doi.org/10.1109/tim.2006.884139>
- [14] J. J. Chen, C.C Chen, H.W. Tsao and S.I. Liu, Current-mode oscillator using single current follower, *Electronics Letters, IEE* (UK), **27** (1991), no. 22, 2056-2059. <http://dx.doi.org/10.1049/el:19911276>
- [15] M. T. Abuelmatti, Grounded-capacitor current-mode oscillator using single current follower, *IEEE Trans. on Circuits and Systems I: Fund. Theo. And App.*, **39** (1992), no. 12, 1018-1020. <http://dx.doi.org/10.1109/81.207726>
- [16] M.T. Abuelmatti, and H.A. AL-Zaher, Current-mode sinusoidal oscillator using single FTFN, *IEEE Trans. on Circuits and Systems II: Anal. and Dig. Sig. Process.*, **46** (1999), no. 1, 69-74. <http://dx.doi.org/10.1109/82.749100>
- [17] S. S. Gupta and R. Senani, Grounded-capacitor current-mode SRCO: Novel application of DVCCC, *Electronics Letters, IEE* (UK), **36** (2000), no. 3, 195-196. <http://dx.doi.org/10.1049/el:20000240>
- [18] C.M. Chang., B.M. AI-Hashimi, H.P. Chen and J.A. Wan, Current-mode single resistance controlled oscillators using only grounded passive components, *Electronics Letters*, **38** (2002), no.19 1071-1072. <http://dx.doi.org/10.1049/el:20020714>
- [19] R. Senani and R. K. Sharma, Explicit-current-output sinusoidal oscillators employing only a single current-feedback op-amp, *IEICE Electron. Express*, **2** (2005), no. 1, 14-18. <http://dx.doi.org/10.1587/elex.2.14>

- [20] V. Aggarawal, S. Kilinc and U. Cam, Minimum component SRCO and VFO using a single DVCCC, *Analog Integrated Circuits and Signal Processing*, **49** (2006), no. 2, 181-185.
<http://dx.doi.org/10.1007/s10470-006-9364-2>

- [21] U. Cam, A. Toker, O. Cicekoglu and H. Kuntman, Current-mode high output impedance sinusoidal oscillator configuration employing single FTFN, *Analog Integr. Circ. and Sig. Process.*, **24** (2000), no. 3, 231-238.
<http://dx.doi.org/10.1023/a:1008365726144>

- [22] M. I. Shaktour, Unconventional circuit elements for ladder filter design, Ph.D Thesis, Brno Univ. Tech.
https://www.vutbr.cz/www_base/zav_prace_soubor_verejne.php?file_id=35974

- [23] G. Singh, D. Prasad and D.R. Bhaskar, Single VDVTA-based voltage-mode biquad filter, *Circuits and Systems*, **6** (2015), no.3, 55-59.
<http://dx.doi.org/10.4236/cs.2015.63006>

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